

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Tohru OZAKI, et al.

SERIAL NO: NEW APPLICATION

GAU: Unassigned

FILED: HEREWITH

EXAMINER: Unassigned

FOR: SEMICONDUCTOR MEMORY DEVICE AND METHOD OF FABRICATING THE SAME

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- ☒ The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references were provided in the prior application serial number 09/956,001, filed 09/20/01, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- ☐ Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- ☒ Please charge any additional fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

  
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Registration No. 28,870



22850

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. <b>240324US-2 TTC DIV</b>		SERIAL NO. <b>NEW APPLICATION</b>	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT <b>Tohru OZAKI, et al.</b>			
				FILING DATE <b>HEREWITH</b>		GROUP <b>Unassigned</b>	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA	5,281,555	01/1994	CHO			
	AB	5,361,234	11/1994	IWASA			
	AC	5,410,161	04/1995	NARITA			
	AD	5,689,126	11/1997	TAKAISHI			
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
<b>FOREIGN PATENT DOCUMENTS</b>							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES                  NO		
	AO	4-05-129552	05-1993	JAPAN			
	AP	4-08-181290	07-1996	JAPAN			
	AQ	4-11-354727	12-1999	JAPAN			
	AR						
	AS						
	AT						
	AU						
	AV						
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
	AW	Takashima et al., A Sub-40ns Random-Access Chain FRAM Architecture with a 7ns Cell-Plate-Line Drive, 1999, IEEE, International Solid State Circuits Conference, pp. 102, 103, 150					
	AX	Takeshima et al., Gain Cell Block Architecture for Gigabit-Scale Chain Ferroelectric RAM, June 1999, Symposium on VLSI Circuits Digest of Technical Papers, pp. 103-04					
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner					Date Considered		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							